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[The Network Weather Service: A Distributed Resource Performance Forecasting Service for ...](#) - [all 20 versions »](#)

R Wolski, N Spring, J Hayes - Journal of Future Generation Computing Systems, 1999 - dsi.unive.it
... indenitely. Each le that a Persistent State process uses is managed as a **circular queue**, the length of which is a conguration option. Data ...
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[... channel FIFO devices using pending channel information stored in a **circular queue** for transfer of ...](#) - [all 3 versions »](#)

UH Nguyen, L Whang, G Apostol - US Patent 5,363,485, 1994 - Google Patents
... [54] BUS INTERFACE HAVING SINGLE AND MULTIPLE CHANNEL FIFO DEVICES USING PENDING CHANNEL INFORMATION STORED IN A **CIRCULAR QUEUE** FOR TRANSFER OF INFORMATION ...
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[Data handling system with **circular queue** formed in paged memory](#) - [all 3 versions »](#)

P Regache - US Patent 5,873,089, 1999 - Google Patents
... [54] DATA HANDLING SYSTEM WITH **CIRCULAR QUEUE** FORMED IN PAGED MEMORY [75] Inventor: Pascal Regache, Fort Collins, Colo. ... **TO CIRCULAR QUEUE** IN PAGED MEMORY i ...
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[Method of management of a **circular queue** for asynchronous access](#) - [all 3 versions »](#)

DJ Garcia, DP Sonnier - US Patent 6,145,061, 2000 - Google Patents
... [54] METHOD OF MANAGEMENT OF A **CIRCULAR QUEUE** FOR ASYNCHRONOUS ACCESS [75] Inventors: David J. Garcia, Los Gatos, Calif.; David P. Sonnier, Austin, Tex. ...
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[... interface using pending channel information stored in single **circular queue** for controlling channels ...](#) - [all 3 versions »](#)

UH Nguyen, L Whang, G Apostol - US Patent 5,450,547, 1995 - Google Patents
... [54] BUS INTERFACE USING PENDING CHANNEL INFORMATION STORED IN SINGLE **CIRCULAR QUEUE** FOR CONTROLLING ... pointer means for traversing **circular queue** means, slot ...
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[Load-balancing clusters in wireless ad hoc networks](#) - [all 10 versions »](#)

AD Amis, R Prakash - Proceedings 3rd IEEE Symposium on Application-Specific ..., 2000 - doi.ieeeecs.org
... The node id load-balancing heuristic operates on the principle of a **circular queue**...
circular queue has a minimum value of 1 and a maximum value of MAX COUNT. ...
[Cited by 67](#) - [Related Articles](#) - [Web Search](#)

[... from a plurality of devices using dual level **queue** locking scheme and a doubly-linked **circular queue**](#) - [all 3 versions »](#)

D Arnon, Y Ofek - US Patent 6,321,308, 2001 - Google Patents
... (54) METHOD AND APPARATUS FOR MANAGING ACCESS REQUESTS FROM A PLURALITY OF DEVICES USING DUAL LEVEL **QUEUE** LOCKING SCHEME AND A DOUBLY-LINKED **CIRCULAR QUEUE** ...

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AX Falcao, JK Udupa, FK Miyazawa - Medical Imaging, IEEE Transactions on, 2000 - [ieeexplore.ieee.org](#)

... an integer in the interval $[0; C]$. Dial's solution uses a **circular queue** with

$C + 1$ buckets of nodes as the priority **queue** of the Dijkstra's algorithm. ...

[Cited by 71](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Cache **queue** entry linking for DASD record updates](#) - [all 2 versions](#) »

SK Candelaria, VJ Legvold, WK Stanley - US Patent 5,682,513, 1997 - Google Patents

... BEFORE TRANSFER ? ACCESS **CIRCULAR QUEUE** TO READ RECORD UPDATE 5,682,513 ... **circular queue** in a storage controller memory for linking header in the cache memory, ...

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H Abdel-Wahab, K Jeffay - INTERNETWORKING RES EXPER, 1994 - [cs.unc.edu](#)

... To address this problem, we use a sequence number mapping technique that uses a

circular queue data structure shown in Figure 2. Upon receiving a request from ...

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[Dynamic restructuring in an experimental operating system](#)

H Goullon, R Isle, KP L  hr - Proceedings of the 3rd international conference on Software ..., 1978 - [portal.acm.org](#)

... END **circular queue** = **queue** OBJECT cell: array (l,size) OF string VAR head, tail,

count: int; ... PROC nonempty = count 0; head:= l; tail:= 1 **circular queue** ...

[Cited by 29](#) - [Related Articles](#) - [Web Search](#)

[Synchronous protocol data formatter](#) - [all 5 versions](#) »

PCJ Chao, BS Choe, RC Fairfield, TL Hiller, RW ... - US Patent 5,029,163, 1991 - Google Patents

... According to another feature of our invention, the formatter includes a **circular**

queue and "interrupt- **queue**" structure that facilitates the shared usage of a ...

[Cited by 22](#) - [Related Articles](#) - [Web Search](#)

[Static instruction decoder utilizing a **circular queue** to decode instructions and select instructions ...](#) - [all 3 versions](#) »

ET Grochowski - US Patent 6,115,807, 2000 - Google Patents

... Grochowski [54] STATIC INSTRUCTION DECODER UTILIZING A **CIRCULAR QUEUE** TO DECODE INSTRUCTIONS AND SELECT INSTRUCTIONS TO BE ISSUED ... UTILIZING A **CIRCULAR QUEUE** TO ...

[Cited by 5](#) - [Related Articles](#) - [Web Search](#)

[... a space appended to a **circular queue** after traversing an end of the **queue** and upon completion ...](#) - [all 3 versions](#) »

WR Etcheverry... - US Patent 6,314,478, 2001 - Google Patents

... (54) SYSTEM FOR ACCESSING A SPACE APPENDED TO A **CIRCULAR QUEUE** AFTER TRAVERSING AN END OF THE **QUEUE** AND UPON COMPLETION COPYING DATA BACK TO THE **QUEUE** ...

[Cited by 5](#) - [Related Articles](#) - [Web Search](#)

[MEMORY-BASED **CIRCULAR QUEUE** WITH LOCAL DESCRIPTIVE INFORMATION TO IMPLEMENT A STORAGE AREA FOR ...](#) - [all 3 versions](#) »

A Movshovich, RH Hoem, NA Puttaswamy, B Lai - US Patent 6,434,170, 2002 - Google Patents

Page 1. US006434170B1 (12) United States Patent Movshovich et al. (54) MEMORY-BASED **CIRCULAR QUEUE** WITH LOCAL DESCRIPTIVE INFORMATION TO IMPLEMENT A STORAGE ...

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Entry allocation in a circular buffer using wrap bits indicating whether a queue of the circular ... - all 3 versions »

DB Papworth, AF Glew, MA Fetterman, GJ Hinton, RP... - US Patent 5,584,038, 1996 - Google Patents
... Bridges 395/410 5,471,598 11/1995 Quattromani 395/449 5,473,756 12/1995 Traylor
395/250 OTHER PUBLICATIONS A Parallel, High Speed **Circular Queue** Structure by ...
[Cited by 35](#) - [Related Articles](#) - [Web Search](#)

Direct memory access unit for transferring data between processor memories in multiprocessing ... - all 3 versions »

LP Andrews, D Arias, BD Mandalia, OE Ortega, JC ... - US Patent 5,634,099, 1997 - Google Patents
... The address of each control block is written to a **circular queue**, also in the remote processor's memory. ... FROM FIG. 3C DSP 0 SPACE **CIRCULAR QUEUE** WORD ADDRS 0 ...
[Cited by 28](#) - [Related Articles](#) - [Web Search](#)

An empirical study of decentralized ILP execution models - all 4 versions »

N Ranganathan, M Franklin - ACM SIGOPS Operating Systems Review, 1998 - portal.acm.org
... proposed so far, such as the multiscalar processor, the superthreading processor, and the trac processors, all organize the PEs as a **circular queue** as shown in ...
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... in a multi-channel system using circular descriptor queue, descriptor FIFO, and receive status queue - all 3 versions »

AJP O'Toole, S Chari... - US Patent 5,870,627, 1999 - Google Patents
... descriptor **queue** 34. The receive descriptor **queue** 34 is a **circular queue** occupying a contiguous area of memory. The location and ...
[Cited by 25](#) - [Related Articles](#) - [Web Search](#)

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CS Lee, FA Itkowsky Jr - US Patent 5,594,927, 1997 - Google Patents
... A **circular queue** is a software array structure of message storage locations. The items in **circular queues** are accessed 65 in first ...
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[CITATION] A Non-Blocking Compare-and-Swap Algorithm for a Shared Circular Queue
JM Stone - Parallel and Distributed Computing in Engineering Systems, 1992
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[BOOK] C++ Object-Oriented Data Structures - all 2 versions »

S Sengupta, CP Korobkin - 1994 - books.google.com
... 253 6.5.2 OOP Implementation of a **Queue** Using Linked List 261 6.6 **Circular Queue** Objects 268 6.6.1 OOP Implementation of a **Circular Queue** Using Array 269 ...
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CD Thompson, JP Gergen - US Patent 4,766,561, 1988 - Google Patents
... filters with simplified state machine control. ** a **circular queue** to simulate a shift register. In other ... **circular queue** for sequentially storing time sampled ...
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Circular queue paradoxes- the missing link - all 2 versions »

L Goldstein - Analysis, 1999 - Blackwell Synergy
... 284-90. © Laurence Goldstein **Circular queue** paradoxes – the missing link Laurence Goldstein ... Page 2. **circular queue** paradoxes – the missing link 285 ...

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[BOOK] Simple, fast, and practical non-blocking and blocking concurrent **queue** algorithms - [all 22 versions »](#)

MM Michael, ML Scott - 1996 - ACM Press New York, NY, USA

... lost permanently. Stone also presents [19] anon-blocking **queue** based on a **circular** singly-linked list. The algorithm uses one anchor ...

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Parameter-controlled volume thinning - [Full Text@IngentaConnect](#) - [all 6 versions »](#)

N Gagvani, D Silver - Graphical Models and Image Processing, 1999 - caip.rutgers.edu

... A **circular queue** is used to keep track of the distance values for successive peeling. ... 2 . In the first pass, the **circular queue** ...

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JM Stone, AN Compare - 1992 - Elsevier Science BV

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E. Leiserson Harald Prokop Sridhar Ramachandran MIT Laboratory ...

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Checkpointing alternatives for high performance, power-aware processors - [all 6 versions »](#)

A Moshovos - Proceedings of the 2003 international symposium on Low power ..., 2003 - portal.acm.org

... This is the functionality provided by checkpointing. Checkpointing is implemented via the re-order buffer (ROB), a **circular queue** which works as follows. ...

[Cited by 10](#) - [Related Articles](#) - [Web Search](#)

Multiscalar processors - [all 31 versions »](#)

GS Sohi, SE Breach, TN Vijaykumar - Computer Architecture, 1995. Proceedings. 22nd Annual ..., 1995 - ieeexplore.ieee.org

... the tasks. The sequential order on the processing units is maintained by organizing the units into a **circular queue**. Head and tail ...

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Dynamic current distribution controls of a parallel connectedconverter system

CQ Lee, K Siri, TF Wu - Power Electronics Specialists Conference, 1991. PESC'91 ..., 1991 - ieeexplore.ieee.org

... operation (first on- converter will be the last off-converter) as shown in [3] and the sequential operation in [4], we propose the **circular queue** operation ...

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Run-time monitoring of real-time systems - [all 5 versions »](#)

SE Chodrow, F Jahanian, M Donner - Real-Time Systems Symposium, 1991. Proceedings., Twelfth, 1991 - ieeexplore.ieee.org

... Each event data structure consists of a finite length **circular queue** of times (and values), with a relative index pointing to the most recent event. ...

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Monitoring system activity for OS-directed dynamic power management - all 7 versions »

L Benini, A Bogliolo, S Cavallucci, B Riccò - Proceedings of the 1998 international symposium on Low power ..., 1998 - portal.acm.org

... of the list-update procedure, NotifyEvent, is shown in Figure 2. The parameters are the event list implemented as a **circular queue**, the uniqueness resource ...

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Realtime hardware scheduler utilizing processor message passing and **queue** management cells - all 3 versions »

CT Johnson, JD Bezek - US Patent 5,832,262, 1998 - Google Patents

... 6, Nov. 1992, p. 469. However, such arrangements are handicapped where they require that each **circular queue** have assigned to it a separate digital processor. ...

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ARB: a hardware mechanism for dynamic reordering of memory references - all 16 versions »

M Franklin, GS Sohi - IEEE Transactions on Computers, 1996 - doi.ieeecs.org

... proceed. Memory operations are assigned a unique row in the dependency matrix; the rows are managed as a **circular queue**. When an ...

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Method of managing hardware control blocks utilizing endless **queue** maintained to never be empty and ... - all 3 versions »

BA Young - US Patent 6,006,292, 1999 - Google Patents

... **queue** 112, HIM 202 replaces the out-of-bounds tail address with the base address of TCB pointer **queue** 112, according to the definition of a **circular queue**....

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Fast object tracking in digital video - all 5 versions »

DK Park, HS Yoon, CS Won - Consumer Electronics, IEEE Transactions on, 2000 - ieeexplore.ieee.org

... requirement. That is, by adopting the **circular queue** as shown in Fig. ... method.

Fig. 16 Structure of priority **circular queue**. Page 6. 790 ...

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Dual pointer **circular queue** - all 3 versions »

ML Buer... - US Patent 6,148,365, 2000 - Google Patents

... Buer [54] DUAL POINTER **CIRCULAR QUEUE** ... FIG. 4 is a diagram the first-in-first-out (FIFO) buffer which functions as a dual pointer **circular queue** shown in 60 ...

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A simple, fast and scalable non-blocking concurrent FIFO **queue** for shared memory multiprocessor ... - all 10 versions »

P Tsigas, Y Zhang - Proceedings of the thirteenth annual ACM symposium on ..., 2001 - portal.acm.org

... based on a singly-linked list. Stone describes a non-blocking algorithm based on a **circular queue**. Massalin and Pu [8] present a ...

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... valid micro operations received from a parallel decoder into adjacent locations of an output **queue** - all 3 versions »

GL Brown, AL Carbine, DD Parker - US Patent 5,673,427, 1997 - Google Patents

... An embodiment described in which the FIFO **queue** includes a **circular queue** with a plurality of entries. ... 4: 1 ALIGNING MUXES **CIRCULAR QUEUE** - 850 4 : 1 802 ...

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F Wieland, D Carnes, G Schultz - Proc. of the 15th Workshop on Parallel and Distributed ..., 2001 - doi.
ieeecomputersociety.org
... As potential conflict pairs are nominated as described above, they are placed in a statically created and sized **circular queue**....
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Interprocessor interrupt processing system - all 2 versions »

LP Andrews, BD Mandalia, OE Ortega, JC Sinibaldi, ... - US Patent 5,553,293, 1996 - Google Patents
... **CIRCULAR QUEUE BUFFER** ... The IIU subsequently accesses the **circular queue** again so that the IIU can fetch the next interrupt control block for processing. ...
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Current distribution control for parallel connected converters. I - all 6 versions »

K Siri, CQ Lee, TE Wu - Aerospace and Electronic Systems, IEEE Transactions on, 1992 - ieeexplore.ieee.org
Page 1. I. I. INTRODUCTION Current Distribution Control For Para1 le1 Connected Converters: Part I K. SIRI, Student, IEEE C. Q. LEE ...
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Architecture for high performance management of multiple **circular** FIFO storage means - all 3 versions »

MM Colmant, RW Krug, AA Smith - US Patent 5,519,701, 1996 - Google Patents
... SUMMARY OF THE INVENTION Accordingly, it is an object of the present invention to provide improved **queue** management for multiple **circular** Page 11. 5,519,701 ...
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SK Lahiri, SA Seshia, RE Bryant - Formal Methods in Computer-Aided Design (FMCAD'02), LNCS, 2002 - Springer
... This includes com- ponents with infinite resources (eg infinite memory) or resources with finite but arbitrary size (eg a **circular queue** of arbitrary length). ...
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Multiple-reader multiple-writer **queue** for a computer system - all 6 versions »

P Drews - US Patent 6,173,307, 2001 - Google Patents
... P3-1.1 to P3-1.4. Ahmed E. Barbour et al., "A Parallel, High Speed **Circular Queue** Structure," 1990, pp. 1089-1092. *cited by examiner ...
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The PaCMAN Metacomputer: parallel computing with Java mobile agents - all 7 versions »

P Evripidou, G Samaras, C Panayiotou, E Pitoura - Future Generation Computer Systems, 2001 - Elsevier
... Enlarge Image (7K). Fig. 1. The PaCMAN Metacomputer. An auxiliary structure of the PaCMAN infrastructure is the virtual **circular queue** (VCQ) (Fig. ...
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Data flow analysis for software prefetching linked data structures in Java - all 24 versions »

B Cahoon, KS McKinley - Proceedings of the 2001 International Conference on Parallel ..., 2001 - doi.
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Page 1. Data Flow Analysis for Software Prefetching Linked Data Structures in Java Brendon Cahoon and Kathryn S. McKinley Department ...
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... system of controlling a dynamically expandable shared **queue** in which ownership of a **queue** entry by a ... - all 4 versions »

KW Holt - US Patent 5,922,057, 1999 - Google Patents
... In the depicted example, **queue** 200 is a **circular queue** although other types of

queues may be implemented according to the present invention. ...

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[\[PS\] A Scalable Flow Control Algorithm for the Fast Messages Communication Library - all 6 versions »](#)
R Canonico, R Cristaldi, G Iannello - Workshop on Communication, Architecture and Applications for ..., 1999 - grid.unina.it

... Since programmed I/O is used, the packets are copied directly from user memory to an adapter's memory region managed as a **circular queue**....

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[Method and network device for creating circular queue structures in shared memory - all 3 versions »](#)

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... <1 (- 73) METHOD AND NETWORK DEVICE FOR CREATING CIRCULAR QUEUE STRUCTURES IN SHARED

MEMORY (US) Assignee: STMicroelectronics, Inc., Carrollton, TX (US) ...

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X Liang, J Jean - IEEE Transactions on Very Large Scale Integration (VLSI) ..., 2003 - ebony.jsums.edu

... A **circular queue** and a multiplexer as shown in Figure 5 are used for this purpose.

Figure 5: **Circular Queue** with Memory Write Scheduling ... 1 0 **Circular Queue** ...

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[Method and apparatus for updating host memory in an adapter to minimize host CPU overhead in ... - all 3 versions »](#)

B Shah, PJ Roman, M Ben-Nun, KK Ramakrishnan - US Patent 5,761,427, 1998 - Google Patents

... FIG. 5 is a block diagram illustrating **circular queue** memory contains aplurality of queues 125. ... number of entries in the **circular queue**. To increment an index. ...

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[BDD vs. Constraint-Based Model Checking: An Experimental Evaluation for Asynchronous Concurrent ... - all 6 versions »](#)

T Bultan - Proc. TACAS, 2000 - Springer

... available = available - 1 e consume : available < size ^ consumed = consumed + 1 ^ available = available + 1 Program: **Circular-Queue** Parameterized Constant ...

[Cited by 6](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Message transfer apparatus for controlling a message send in a packet switched interconnection ... - all 3 versions »](#)

SM Moh, SS Shin, SH Yoon, KW Rim - US Patent 6,023,732, 2000 - Google Patents

... MCR 203 makes four unit buffers MBO 209, MB1 210, MB2 211 and MBS 212 which consist of the message buffer 207 as a **circular queue** structure so as to control them ...

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[Real-time platforms and environments for time constrained flexible manufacturing](#)

JA Stankovic, K Ramamritham, G Zlokapa - Real-Time Operating Systems and Software, 1994. RTOSS'94, ..., 1994 - ieeexplore.ieee.org

... In this testbed (see Figure 1), objects or material needed by consuming processes are introduced non- deterministically into a **circular queue** awaiting pro ...

[Cited by 4](#) - [Related Articles](#) - [Web Search](#)

[A New Approach for Scheduling of Parallelizable Tasks in Real-Time Multiprocessor Systems - all 6 versions »](#)

G Manimaran, CSR Murthy, K Ramamritham - Real-Time Systems, 1998 - Springer

... end. Figure 1a. Schedulability check algorithm. tasks, currently in the task **queue**, with $r_1 = 8$, $c_1 = 12$, $c_2 = 7$, $c_3 = 5$, $d_1 = 16$, and ...

[Cited by 8](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Perils of the PC Cache](#) - [all 2 versions](#) »

P Koopman - Embedded Systems Programming, 1993 - ece.cmu.edu

... Let's say you want to read data in and put it in a **circular queue** with a really simple interrupt service routine (ISR) written in Turbo C: ...

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[INTEGRATING INFORMAL AND FORMAL TECHNIQUES TO REVERSE ENGINEER IMPERATIVE PROGRAMS](#) - [all 3 versions](#) »

GC Gannod - 1998 - users.muohio.edu

... 251 D.2 **Circular Queue** Library Specification

148 9.5 **Circular Queue** Diagram

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[Method and structure for balanced queue communication between nodes in a distributed computing ...](#)

- [all 4 versions](#) »

HA Piskiel, GS Mui, PHN Pelizzoli - US Patent 5,916,307, 1999 - Google Patents

... The position in the originating node's **circular queue** is pre-pended to the message and transmitted therewith. The receiving node ...

[Cited by 24](#) - [Related Articles](#) - [Web Search](#)

[Synthesized stereoscopic imaging system and method](#) - [all 5 versions](#) »

BJ Garcia... - US Patent 5,510,832, 1996 - Google Patents

Page 1. United States Patent Garcia US005510832A [ii] Patent Number: [45] Date of Patent: [54] SYNTHESIZED STEREOSCOPIC IMAGING SYSTEM AND METHOD ...

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[\[PS\] Stream Sockets on SHRIMP](#) - [all 5 versions](#) »

SN Damianakis, C Dubnicki, EW Felten - Communication, Architecture, and Applications for Network- ..., 1997 - cs.princeton.edu

... In order to implement a simple unidirectional byte stream the sender and receiver each allocate a copy of the **circular queue** and cross map the three components ...

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[A tmo-based approach to tolerance of transmission jitters in tele-audio services](#)

DH Kim, KH Kim, S Liu, JH Kim - Journal of Computer Systems Science and Engineering, 2002 - ai.kaist.ac.kr

... D - H KIM ET AL Figure 2 A simple TMO network using low-precision-capture-time-stamping Page 5. ODS which is used to hold the audio data is a **circular queue**....

[Cited by 5](#) - [Related Articles](#) - [View as HTML](#) - [Web Search](#) - [BL Direct](#)

[Shared-carrier frequency hopping](#) - [all 4 versions](#) »

MD Kotzin, SL Spear - US Patent 5,301,188, 1994 - Google Patents

... must be done, each of the nine coverage areas is allocated a unique time or index offset for sequentially indexing into a **circular queue** (or otherwise ...

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[Inverse multiplexing within asynchronous transfer mode communication networks](#) - [all 3 versions](#) »

B Cai, PD Samudra... - US Patent 6,134,246, 2000 - Google Patents

... 30 balancing algorithm at the transmitter, and a **circular** achieve high-speed and flexible communications. ... Such re-sequencing may be performed via a **circular** ...

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[PS] Implementing Fair Queueing in ATM Switches—Part 2: The Logarithmic Calendar Queue

FM Chiu, A Francini, JG Kneuer - Proc. GLOBECOM, 1997 - bell-labs.com

... of interest for the timestamps is a timeslot, one bin must be associated with each timeslot, and the total number of bins in the **circular queue** is given by the ...

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Managing data storage in the network - all 14 versions »

JS Plank, A Bassi, T Moore, M Swamy, R Wolski - Internet Computing, IEEE, 2001 - ieeexplore.ieee.org

... Finally, the byte array type can be standard (append-only file), pipe (fixed-size FIFO buffer), or **circular queue** (bounded buffer with destructive writes). ...

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Dynamic addressing memory arrays with physical locality - all 5 versions »

S Hsu, SL Lu, SC Lai, R Krishnamurthy, K Lai - Microarchitecture, 2002.(MICRO-35). Proceedings. 35th Annual ..., 2002 - ieeexplore.ieee.org

... This method checks the physical register file, which is organized as a **circular queue** at the decode time. Whenever a slower entry ...

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Design considerations for shared memory multiprocessor messagesystems - all 5 versions »

N Islam, RH Campbell - Parallel and Distributed Systems, IEEE Transactions on, 1992 - ieeexplore.ieee.org

... This alternative invalidates a cache less frequently than the **circular queue** scheme but creates more contention for acquiring a reused **queue** entry. ...

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A circuit level implementation of an adaptive issue queue for power-aware microprocessors - all 6 versions »

A Buyuktosunoglu, D Albonesi, S Schuster, D Brooks ... - Proceedings of the 11th Great Lakes symposium on VLSI, 2001 - portal.acm.org

... The issue **queue** is designed to be a **circular queue** structure, with head and tail pointers, and the effective size is dynamically adapted to fit the ILP (In ...

[Cited by 43](#) - [Related Articles](#) - [Web Search](#)

The high-bandwidth 256 kB 2nd level cache on an Itaniummicroprocessor - all 4 versions »

R Riedlinger, T Grutkowski - Solid-State Circuits Conference, 2002. Digest of Technical ..., 2002 - ieeexplore.ieee.org

... The ordering **queue** is a 32 entry out of order **circular queue** that utilizes a dynamic find first four ripple circuit to issue four non-bank-conflicted accesses ...

[Cited by 15](#) - [Related Articles](#) - [Web Search](#)

Method and apparatus for digital sampling of electrical waveforms - all 3 versions »

JJ Pickerd - US Patent 5,978,742, 1999 - Google Patents

... Digital sampling oscilloscopes often provide for an ET mode. An acquisition memory register is typically arranged in a **circular queue**....

[Cited by 28](#) - [Related Articles](#) - [Web Search](#)

Method and apparatus for transporting messages between processors in a multiple processor system - all 6 versions »

WT Futral, E Garbus, B Davis - US Patent 5,925,099, 1999 - Google Patents

... FIG. 5 further illustrates **circular queue** operation for the ... **circular queue** and range from 0 to the **circular queue** size 214 through data path 352. ...

[Cited by 22](#) - [Related Articles](#) - [Web Search](#)

Cache-Oblivious Algorithms - all 30 versions »

H Prokop - 1999 - course.bnu.edu.cn

Page 1. Cache-Oblivious Algorithms by Harald Prokop Submitted to the Department of Electrical Engineering and Computer Science in ...

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[Automated function testing of application programs - all 2 versions »](#)

G Triantafyllos, WH Shield - US Patent 5,233,611, 1993 - [Google Patents](#)

Page 1. United States Patent Triantafyllos et al. [54] [75] AUTOMATED FUNCTION TESTING OF APPLICATION PROGRAMS Inventors: George ...

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[Delaunay triangulation using a uniform grid - all 7 versions »](#)

TP Fang, LA Piegl - IEEE Computer Graphics and Applications, 1993 - doi.ieeecomputersociety.org

... The algo- rithm subdivides the edge and puts the two half edges P,Pz and

PIPI into an edge list that is actually a **circular queue**...

[Cited by 59](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Method and apparatus for placing multiple frames of data in a buffer in a direct memory access ... - all 3 versions »](#)

AJP O'Toole, S Chari... - US Patent 5,828,901, 1998 - [Google Patents](#)

... locations of a **circular** descriptor **queue**, obtaining with a cessor ... recep- **circular queue** occupying a contiguous area of memory. The ...

[Cited by 26](#) - [Related Articles](#) - [Web Search](#)

[A flexible broadband packet switch for a multimedia integrated network](#)

CB Woodworth, MJ Karol, RD Gittlin - Communications, 1991. ICC 91, Conference Record. IEEE ..., 1991 - ieeexplore.ieee.org

... in Fig. 6. These pointers implement a **circular queue** so that cells retain their first-in first-out sequence. Each address pointer ...

[Cited by 33](#) - [Related Articles](#) - [Web Search](#)

[Apparatus and method for controlling interrupts to a host during data transfer between the host and ... - all 2 versions »](#)

S Chmielecki Jr, FA Itkowsky Jr, GP Koning, DM ... - US Patent 5,740,467, 1998 - [Google Patents](#)

... No. **circular** queues in host memory, and the value of the FIELD OF THE INVENTION

a **circular queue**... fl newi(emwm neyerbevniKen jntothe **circular queue** ...

[Cited by 15](#) - [Related Articles](#) - [Web Search](#)

[Method for controlling a message send in a packet-switched interconnection network - all 3 versions »](#)

SM Moh, SS Shin, SH Yoon, KW Rim - US Patent 5,910,178, 1999 - [Google Patents](#)

... in accordance with the result of checking said IE bit; and (9) after asserting a transfer interrupt to a processor, executing a **circular queue** operation by a ...

[Cited by 4](#) - [Related Articles](#) - [Web Search](#)

[Development of expert control systems: A pattern classification and recognition approach](#)

MS Mahmoud, AA Abou-Elseoud, S Kotob - Journal of Intelligent and Robotic Systems, 1992 - Springer

... The implementation involves a pattern recognition classification and a **circular queue** is used to store the process output. The average ...

[Cited by 4](#) - [Related Articles](#) - [Web Search](#)

[ATM switch with integrated system bus - all 2 versions »](#)

K Norton, M Rumer... - US Patent 5,898,688, 1999 - [Google Patents](#)

... tRequest/Status **Circular Queue** 32 Word x 32 Bit 6O ... 11 shows the request/status **circular queue** for an ATM cell processing unit according to one embodiment; FIG. ...

[Cited by 15](#) - [Related Articles](#) - [Web Search](#)

Implementation and Analysis of the User Direct Access Programming Library - all 2 versions »

J Lentini, V Pham, S Sears, R Smith - 2nd Workshop on Novel Uses of System Area Networks, SAN, 2003 - ics.uci.edu

... associated with that structure. In the reference implementation, that structure is a producer/consumer **circular queue**. When an event is ...

[Cited by 3](#) - [Related Articles](#) - [View as HTML](#) - [Web Search](#)

An Adaptive Issue Queue for Reduced Power at High Performance - all 14 versions »

A Buyuktosunoglu, S Schuster, D Brooks, P Bose, P ... - Proceedings of the First International Workshop on Power- ..., 2000 - Springer

... The issue **queue** is designed to be a **circular queue** structure, with head and tail pointers, and the effective size is dynamically adapted to fit the ILP content ...

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The Object Library for Parallel Simulation (OLPS) - all 4 versions »

M Abrams - Simulation Conference Proceedings, 1988 Winter, 1988 - ieeexplore.ieee.org

... able. The CQCHANNEL provides a monitor to access object CIRCULARQ, which is a **circular queue** of fixed, user specified size. The ...

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The weighted-distance scheme: a globally optimizing projectionordering method for ART - all 9 versions »

K Mueller, R Yagel, JF Cornhill - Medical Imaging, IEEE Transactions on, 1997 - ieeexplore.ieee.org

... The algorithm implements the permutation of the projection ordering as a **circular queue** 2. The length S of 2 depends on how much of the projection access ...

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Dynamic allocation of registers to procedures in a digital computer - all 2 versions »

FC Amerson, RM English, R Gupta, T Watanabe - US Patent 5,564,031, 1996 - Google Patents

... Primary Examiner—William M. Treat Assistant Examiner—Kenneth R. Coulter [57] ABSTRACT In a digital computer, a **circular queue** of registers in a register ...

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Client-server computing on Shrimp - all 4 versions »

SN Damianakis, A Biles, C Dubnicki, EW Felten - Micro, IEEE, 1997 - ieeexplore.ieee.org

... To implement a simple unidirectional byte stream, the sender and receiver each allocate a copy of the **circular queue** and build several import-export mappings. ...

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The TMS 320 C 30 floating-point digital signal processor - all 9 versions »

P Papamichalis, R Simar - IEEE Micro, 1988 - doi.ieeecomputersociety.org

... Indirect ADDF * AR0 + + (di)% ,RI Addr = AR0 Postdisplacement add AR0 = circ(AR0 + di) and **circular** modify ... **Circular** addressing implements **circular** buffers. ...

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[BOOK] Coherent network interfaces for fine-grain communication - all 12 versions »

SS Mukherjee, B Falsafi, MD Hill, DA Wood - 1996 - ACM Press New York, NY, USA

... cachable device registers from one cachable, coherent memory block to a contiguous region of cachable, coherent blocks managed as a **circular queue**....

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Control Flow Speculation in Multiscalar Processors - all 13 versions »

Q Jacobson, S Bennett, N Sharma, JE Smith - HPCA-3 proceedings, 1996 - meseec.ce.rit.edu

... The ring operates as a **circular queue** with a head and a tail pointer (pointing to the current non-speculative task and the most recently started speculative ...

[Cited by 66](#) - [Related Articles](#) - [View as HTML](#) - [Web Search](#)

Combination router and cluster controller - all 2 versions »

S Eisenhandler, T Chaudhry, X Lee, PS Lam, Y Fujita ... - US Patent 5,452,291, 1995 - Google Patents
... 506 LOAD COUNTER WITH NEXT SYMBOL AND RESTART 504 508)START BUS QUIET COUNTING
PROCESS **QUEUE** NEXT SYMBOL FIG. 14 510 RETURN 514 Page 13. US Patent Sep. ...

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Vehicular emergency message system activation diagnostics recorder - all 2 versions »

G Stephen, WA Dorfstatter, MJ Timm - US Patent 5,781,101, 1998 - Google Patents

... system. j- <. • 6 response center a priority of a respective activation.

A 'diagnostic memory comprises a **circular queue** storing pre- ...

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Improving the Throughput of Remote Storage Access through Pipelining - all 9 versions »

E Nallipogu, F Ozguner, M Lauria - Proceedings of the Third International Workshop on Grid ..., 2002 - Springer

... The server maintains a **circular queue** of buffers, one buffer for each outstanding

read. ... A **circular queue** is used to keep track of outstanding writes. ...

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A parallel, high speed **circular queue** structure

AE Barbour, I Alhayek - Circuits and Systems, 1989., Proceedings of the 32nd Midwest ..., 1989 - ieeexplore.
ieee.org

Page 1 A PARALLEL, HIGH SPEED **CIRCULAR QUEUE** STRUCTURE 89CH2785.4/90/OOoO-1o8ito ©
1990 IEEE Ahmed E. Barbour lyad Aihayek t University of Illinois at Chicago ...

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[PS] Parameter controlled skeletonization of three dimensional objects - all 3 versions »

N Gagvani, D Silver - 1997 - caip.rutgers.edu

Page 1. Technical Report CAIP-TR-216 PARAMETER CONTROLLED SKELETONIZATION
OF THREE DIMENSIONAL OBJECTS Nikhil Gagvani and Deborah ...

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Single chip protocol engine and data formatter apparatus for off chip host memory to local memory ... -
all 3 versions »

DM Weber, TE Hoglund, SM Johnson, JM Adams, MA ... - US Patent 6,185,620, 2001 - Google Patents

... MF N TFREE FIFO REQUEST MESSAGE FRAME **QUEUE** US Patent Feb. 6, 2001 Sheet 3 of 13

US 6,185,620 B1 704 Si MFA_1 ... MF_2 MF 1 REPLY MESSAGE FRAME **QUEUE** -1 200 1202 ...

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A first implementation of in-transit buffers on myrinet gm software - all 11 versions »

S Coll, J Flich, MP Malumbres, P Lopez, J Duato, ... - Parallel and Distributed Processing Symposium.,
Proceedings ..., 2001 - ieeexplore.ieee.org

... This buffer pool could be implemented as a **circular queue** (as many commercial network
adapters do) managed with two pointers: one pointing the first incoming ...

[Cited by 11](#) - [Related Articles](#) - [Web Search](#)

Structured message passing on a shared-memory multiprocessor - all 2 versions »

TJ LeBlanc - System Sciences, 1988. Vol. II. Software Track, Proceedings ..., 1988 - ieeexplore.ieee.org

Page 1 TH0212-1/88/0000/0188\$0100 © 1988 IEEE Structured Message Passing on a
Shared-Memory Multiprocessor Thomas J. LeBlanc Computer Science Department ...

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